



S/N 10/081,818

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Jerome M. Eldridge et al. Examiner: Tu-Tu V. Ho  
Serial No.: 10/081,818 Group Art Unit: 2818  
Filed: February 20, 2002 Docket: 1303.045US1  
Title: ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS

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**COMMUNICATION CONCERNING RELATED APPLICATIONS**

**MS RCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related applications in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
11/704,458	February 9, 2007	1303.045US4	DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS
11/708,438	February 20, 2007	1303.045US5	LOW TUNNEL BARRIER INSULATORS
11/728,671	March 27, 2007	1303.045US6	ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS
11/731,712	March 30, 2007	1303.132US2	GERMANIUM-SILICON-CARBIDE FLOATING GATES IN MEMORIES
11/731,255	March 30, 2007	1303.132US3	GERMANIUM-SILICON-CARBIDE FLOATING GATES IN MEMORIES

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INSULATORS

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Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

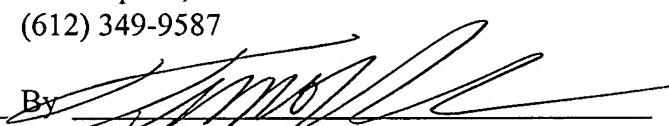
Respectfully submitted,

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June '07 By   
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 7 day of June, 2007.

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